ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	A Digitial Stream Transcoder with a Hybrid-Rate Controller

Application Number :   10/658131

Confirmation Number: 5432

First Named Applicant: Joel Schoenblum

Attorney Docket Number: A-9141

Art Unit:

Examiner:

Search string: (US62086B or US64666B or US61414A).pn

Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
ᴊᴧ	1	US62086B	2001-03-27	Seo et al		375	240.03
	2	US64666B	2002-10-15	Youn et al		375	240.16
	3	US61414A	2000-10-31	Linzer et al		382	236

Signature

Examiner Name	Date
A. ꞁLAo	5/8/06

Form PTO-1449	U.S. Dept. of Commerce, Patent and Trademarks

LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary

Attorney Docket No. A-8115	Serial No. UNKNOWN	Filing Date MARCH 26, 2003

Applicant: SCHOENBLUM	Group UNKNOWN

U.S. PATENT DOCUMENTS

Examiner's Initial		Document No.	Date	Name	Class	Subclass	
	AR						
	AS						
	AT						
	AU						
	AV						
	AW						
	AX						
	AY						
	AZ						
	AAA						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Subclass	Translation
	ABB						
	ACC						
	ADD						
	AEE						
	AFF						

OTHER PRIOR ART (Including Author Title, Date, Pertinent Pages, Etc.)

AK	AGG	"DSP-Based Transcoding of Digital Video Signals with MPEG-2 Format," Bernd Sostawa, et al.l, IEEE Transactions on Consumer Electronics, Vol. 46, No. 2, May 2000, pp. 358-362.
AR	AHH	"MPEG-2 Multi-Program Transport Stream Transcoder," Takeshi Takahashi, et al., Waseda University, Japan, pp. 1-5.
	AII	
	AJJ	
	AKK	

EXAMINER *A. RAO*	DATE CONSIDERED: *5/9/06*

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance. Include copy of this form with next communication to Applicant.

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Attorney Docket No. A-8115	Serial No. UNKNOWN	Filing Date MARCH 26, 2003
Applicant: SCHOENBLUM	Group UNKNOWN	

U.S. PATENT DOCUMENTS

Examiner's Initial		Document No.	Date	Name	Class	Subclass	
Ad	AA	5,870,146	2/9/99	Zhu	178	405	
	AB	6,058,143	5/2/00	Golin	375	240	
	AC	6,081,295	6/27/00	Adolph et al.	348	405	
	AD	6,141,447	10/31/00	Linzer et al.	382	236	
	AE	6,259,741	7/10/01	Chen et al.	375	240.26	
	AF	6,275,536	8/14/01	Chen et al.	375	240.25	
	AG	6,400,763	6/4/02	Wee	375	240.16	
	AH	6,407,681	6/18/02	Gatepin et al.	341	63	
	AI	6,434,197	8/13/02	Wang et al.	375	240.29	
	AJ	6,441,754	8/27/02	Wang et al.	341	50	

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		Document No.	Date	Country	Class	Subclass	Translation
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	AL						

OTHER PRIOR ART (Including Author Title, Date, Pertinent Pages, Etc.)

Ad	AM	"A Frequency-Domain Video Transcoder for Dynamic Bit-Rate Reduction of MPEG-2 Bit Streams," Transaction Papers, Pedro A.A. Assuncao and Mohammed Ghanbari, IEEE Transactions on Circuits and Systems for Video Technology, Vol. 8, No. 8, December 1998, pp. 953-967.
	AN	"Manipulation and Compositing of MC-DCT Compressed Video," Shih-Fu Chang and David G. Messerschmitt, IEEE Journal on Selected Areas in Communications, Vol. 13, No. 1, January 1995, pp. 1-11.
	AO	"THAM 15.5, A low-complexity MPEG-2 bit-rate transcoding algorithm," R. Klein Gunnewick, et al., 2000 IEEE, pp. 316-317.
	AP	"Local Bandwidth Constrained Fast Inverse Motion Compensation for DCT-Domain Video Transcoding," Shizhong Liu and Alan C. Bovik, The University of Texas at Austin, TX, 2001 IEEE, pp. 1609-1612.
	AQ	"A Fast Algorithm for DCT-Domain Inverse Motion Compensation Based on Shared Information in a Macroblock," Junehwa Song and Boon-Lock Yeo, IEEE Transactions on Circuits and Systems for Video Technology, Vol. 10, No. 5, August 2000, pp. 767-775.

EXAMINER A. Kao	DATE CONSIDERED: 5/8/06

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